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Lists citations with abstracts for aerospace related reports obtained from world wide sources and announces documents that have recently been entered into the NASA Scientific and Technical Information Database.

The purpose of this book is to illustrate the magnificence of the fabless semiconductor ecosystem, and to give credit where credit is due. We trace the history of the semiconductor industry from both a technical and business perspective. We argue that the development of the fabless business model was a key enabler of the growth in semiconductors since the mid-1980s. Because business models, as much as the technology, are what keep us thrilled with new gadgets year after year, we focus on the evolution of the electronics business. We also invited key players in the industry to contribute chapters. These "In Their Own Words" chapters allow the heavyweights of the industry to tell their corporate history for themselves, focusing on the industry developments (both in technology and business models) that made them successful, and how they in turn drive the further evolution of the semiconductor industry.

Foldable Flex and Thinned Silicon Multichip Packaging Technology presents newly emerging methods used to make stacked chip packages in the so-called 2-1/2 D technology (3-D in physical format, but interconnected only through the circuits on folded flex). It is also being used in single chip packages where the thinness of the chips and the flex substrate made packages significantly thinner than through any other means.

The third international conference devoted to picosecond phenomena was held June 16-18, 1982 in Garmisch-

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Partenkirchen, West Germany. Scientists from widely varying disciplines, physicists, chemists, biologists, and engineers came together to share their common interest in picosecond and subpicosecond processes. The meeting attracted approximately 250 scientists from numerous countries around the globe. More than 100 papers were concerned with the latest advances in the experimental and theoretical understanding of ultrafast phenomena. New discoveries in femtosecond and picosecond pulse generation and new results in chemical dynamics, solid-state physics, and nonlinear optics were presented. The quality of the scientific reports, the enthusiasm of the participating scientists, as well as the magnificent surroundings of the Bavarian alps guaranteed a successful and pleasant conference. Numerous people have helped to make the conference a success. Special thanks are due to Carin von Oberkamp for doing a superb job in implementing the meeting arrangements and to the program committee for the selection and organisation of the scientific presentations. The financial support of the Deutsche Forschungsgemeinschaft and of the Bayerische Staatsministerium für Unterricht und Kultur is gratefully acknowledged.

Here is the first book to focus solely on Cisco network hacking, security auditing, and defense issues. Using the proven Hacking Exposed methodology, this book shows you how to locate and patch system vulnerabilities by looking at your Cisco network through the eyes of a hacker. The book covers device-specific and network-centered attacks and defenses and offers real-world case studies.

New design architectures in computer systems have surpassed industry expectations. Limits, which were once thought of as fundamental, have now been broken. Digital Systems and Applications details these innovations in systems design as well as cutting-edge applications that are

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emerging to take advantage of the fields increasingly sophisticated capabilities. This book features new chapters on parallelizing iterative heuristics, stream and wireless processors, and lightweight embedded systems. This fundamental text— Provides a clear focus on computer systems, architecture, and applications Takes a top-level view of system organization before moving on to architectural and organizational concepts such as superscalar and vector processor, VLIW architecture, as well as new trends in multithreading and multiprocessing. includes an entire section dedicated to embedded systems and their applications Discusses topics such as digital signal processing applications, circuit implementation aspects, parallel I/O algorithms, and operating systems Concludes with a look at new and future directions in computing Features articles that describe diverse aspects of computer usage and potentials for use Details implementation and performance-enhancing techniques such as branch prediction, register renaming, and virtual memory Includes a section on new directions in computing and their penetration into many new fields and aspects of our daily lives

Develop the software and hardware you never think about. We're talking about the nitty-gritty behind the buttons on your microwave, inside your thermostat, inside the keyboard used to type this description, and even running the monitor on which you are reading it now. Such stuff is termed embedded systems, and this book shows how to design and develop embedded systems at a professional level. Because yes, many people quietly make a successful career doing just that. Building embedded systems can be both fun and intimidating. Putting together an embedded system requires skill sets from multiple engineering disciplines, from software and hardware in particular. Building Embedded Systems is a book about helping you do things in the right way from the beginning of

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your first project: Programmers who know software will learn what they need to know about hardware. Engineers with hardware knowledge likewise will learn about the software side. Whatever your background is, *Building Embedded Systems* is the perfect book to fill in any knowledge gaps and get you started in a career programming for everyday devices. Author Changyi Gu brings more than fifteen years of experience in working his way up the ladder in the field of embedded systems. He brings knowledge of numerous approaches to embedded systems design, including the System on Programmable Chips (SOPC) approach that is currently growing to dominate the field. His knowledge and experience make *Building Embedded Systems* an excellent book for anyone wanting to enter the field, or even just to do some embedded programming as a side project. What You Will Learn Program embedded systems at the hardware level Learn current industry practices in firmware development Develop practical knowledge of embedded hardware options Create tight integration between software and hardware Practice a work flow leading to successful outcomes Build from transistor level to the system level Make sound choices between performance and cost Who This Book Is For Embedded-system engineers and intermediate electronics enthusiasts who are seeking tighter integration between software and hardware. Those who favor the System on a Programmable Chip (SOPC) approach will in particular benefit from this book. Students in both Electrical Engineering and Computer Science can also benefit from this book and the real-life industry practice it provides.

An essential book for 3rd party developers and others interested in products using the PowerPC including those from IBM, Apple, and many other vendors. The book covers the architecture for the entire family of processors from either IBM or Motorola and is the official documentation of the IBM

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reference manual.

This text describes the functions that the BIOS controls and how these relate to the hardware in a PC. It covers the CMOS and chipset set-up options found in most common modern BIOSs. It also features tables listing error codes needed to troubleshoot problems caused by the BIOS.

One problem with helicoptering is that there are virtually no flying clubs, at least of the sort that exist for fixed wing, so pilots get very little chance to swap stories, unless they meet in a muddy field somewhere, waiting for their passengers. As a result, the same mistakes are being made and the same lessons learnt separately instead of being shared - it's comforting sometimes to know that you're not the only one to inflate the floats by accident! Even when you do get into a school, there are still a couple of things they don't teach you, namely that aviation runs on paperwork, and how to get a job, including interview techniques, etc - flying the aircraft is actually less than a third of the job. Another is that nobody really tells you anything, either about the job you have to do (from the customer) or how to do it (the company) - you will always be up against the other guy who managed to do it last week! Sure, there will be training, but, even in the best companies, this will be relatively minimal. This book is an attempt to correct the above situations by gathering together as much information as possible for helicopter pilots, old and new, professional and otherwise, in an attempt to explain the why, so the how will become easier (you will be so much more useful if you know what the customer is trying to achieve). In short, this is all the stuff nobody taught me - every tip and trick I have learnt has been included.

This second edition of *An Engineer's Guide to Automated Testing of High-Speed Interfaces* provides updates to reflect current state-of-the-art high-speed digital testing with automated test equipment technology (ATE). Featuring clear

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examples, this one-stop reference covers all critical aspects of automated testing, including an introduction to high-speed digital basics, a discussion of industry standards, ATE and bench instrumentation for digital applications, and test and measurement techniques for characterization and production environment. Engineers learn how to apply automated test equipment for testing high-speed digital I/O interfaces and gain a better understanding of PCI-Express 4, 100Gb Ethernet, and MIPI while exploring the correlation between phase noise and jitter. This updated resource provides expanded material on 28/32 Gbps NRZ testing and wireless testing that are becoming increasingly more pertinent for future applications. This book explores the current trend of merging high-speed digital testing within the fields of photonic and wireless testing.

The Newnes Know It All Series takes the best of what our authors have written to create hard-working desk references that will be an engineer's first port of call for key information, design techniques and rules of thumb. Guaranteed not to gather dust on a shelf! Circuit design using microcontrollers is both a science and an art. This book covers it all. It details all of the essential theory and facts to help an engineer design a robust embedded system. Processors, memory, and the hot topic of interconnects (I/O) are completely covered. Our authors bring a wealth of experience and ideas; this is a must-own book for any embedded designer. *A 360 degree view from best-selling authors including Jack Ganssle, Tammy Noergard, and Fred Eady *Key facts, techniques, and applications fully detailed *The ultimate hard-working desk reference: all the essential information, techniques, and tricks of the trade in one volume

Sampled Data Systems - ADCs for DSP Applications - DACs for DSP Applications - Fast Fourier Transforms - Digital Filters - DSP Hardware - Interfacing to DSPs - DSP

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Applications - Hardware Design Techniques.

This book constitutes the refereed proceedings of the 17th International Workshop on Cryptographic Hardware and Embedded Systems, CHES 2015, held in Saint Malo, France, in September 2015. The 34 full papers included in this volume were carefully reviewed and selected from 128 submissions. They are organized in the following topical sections: processing techniques in side-channel analysis; cryptographic hardware implementations; homomorphic encryption in hardware; side-channel attacks on public key cryptography; cipher design and cryptanalysis; true random number generators and entropy estimations; side-channel analysis and fault injection attacks; higher-order side-channel attacks; physically unclonable functions and hardware trojans; side-channel attacks in practice; and lattice-based implementations.

Semiconductor sensors patterned at the micron scale combined with custom-designed integrated circuits have revolutionized semiconductor radiation detector systems. Designs covering many square meters with millions of signal channels are now commonplace in high-energy physics and the technology is finding its way into many other fields, ranging from astrophysics to experiments at synchrotron light sources and medical imaging. This book is the first to present a comprehensive discussion of the many facets of highly integrated semiconductor detector systems, covering sensors, signal processing, transistors and circuits, low-noise electronics, and radiation effects. The diversity of design approaches is illustrated in a chapter describing systems in high-energy physics, astronomy, and astrophysics. Finally a chapter "Why things don't work" discusses common pitfalls. Profusely illustrated, this book provides a unique reference in a key area of modern science.

An Engineer's Guide to Automated Testing of High-Speed

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Interfaces, Second Edition Artech House

The first half of "PROTOTYPICAL" is a concise history of FPGA-based prototyping. We go back to the beginning, briefly introducing the debut of the Altera EP300 in 1984 and the Xilinx XC2064 in 1985. We then discuss the tipping point for what would become FPGA-based prototyping: the introduction of the Quickturn Systems RPM in May 1988. Strictly speaking, the RPM was an FPGA-based hardware emulator, but it set the stage for a radical change in chip development methodology. Intel took the Quickturn technology and put the P5 microarchitecture through its paces on a 14-machine cluster, running a killer demo in 1991 and ultimately releasing the Pentium microprocessor in 1993. From there, while the large EDA firms scuffled over bigger and bigger hardware emulation capability, several academic teams started deploying FPGAs for reconfigurable computing and rapid prototyping. These teams were looking for lower cost ways to prove out algorithms and chip designs. It was during this period issues of FPGA interconnect and synthesis partitioning were uncovered and addressed - and just in time, as ARM7TDMI synthesizable cores appeared in 1997. We then launch into chapters with brief timelines of three of the major firms in FPGA-based prototyping: S2C, Synopsys, and Cadence. We close the first half with a look at where FPGA-based prototyping is headed, including how it can help application segments such as automotive, wearables, and the IoT - three segments we believe will see an increasing number of design starts as new players seek to optimize and differentiate their software through chip design. The second half of "PROTOTYPICAL" is an all-new Field Guide titled "Implementing an FPGA Prototyping Methodology" authored by the teams at S2C. It looks at when design teams need an FPGA-based prototyping solution, how to choose one, and how to be sure the platform is scalable including a look at the

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latest cloud-based implementations. It then dives into the methodology: setting up a prototype, partitioning, interconnect, debugging, and exercising a design. It's a practical view of the questions teams have and the issues they run into, and how to solve them.

This book presents selected contributions of the Ultra-Wideband Short-Pulse Electromagnetics 7 Conference, including electromagnetic theory, scattering, Ultrawideband (UWB) antennas, UWB systems, ground penetrating radar, UWB communications, pulsed-power generation, time-domain computational electromagnetics, UWB compatibility, target detection and discrimination, propagation through dispersive media, and wavelet and multi-resolution techniques.

The Most Complete, Current Guide to Troubleshooting and Repairing Electrical and Electronic Devices "If it's electronic, and there is troubleshooting to be done, then this is the book to reach for!" --Dr. Simon Monk, bestselling author of 30 Arduino Projects for the Evil Genius and Hacking Electronics: An Illustrated DIY Guide for Makers and Hobbyists "...an outstanding book on electronic troubleshooting with clear, concise, and concrete examples that anyone can relate to." --James Karagiannes, Ph.D. Physics, Associate Dean of Engineering and Information Sciences, DeVry University, Chicago Fully updated for the latest technologies, devices, test instruments, and problem-solving methods, the new edition of this practical resource provides you with the comprehensive information you need to troubleshoot today's electrical and electronic equipment. Inside you'll find new and enhanced coverage of: Wireless communications Embedded microprocessor systems Cutting-edge medical diagnostic equipment Advanced networking technologies The book uniquely blends traditional electrical theory and components with modern networking and electronic technology. Chapter-

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ending questions and problems test your understanding of the topics discussed. Filled with tables, charts, illustrations, graphs, and flowcharts, this is a must-have manual for anyone who works with electronics--at home or on the job. Electronic Troubleshooting, Fourth Edition, covers: Electric motors and generators Industrial controls Residential, commercial, and wireless communications Radio and television Digital circuits Combinational and sequential digital circuits Microprocessor-based systems Biomedical equipment Computer networking and network drives Embedded microprocessor systems

High Speed Digital Design discusses the major factors to consider in designing a high speed digital system and how design concepts affect the functionality of the system as a whole. It will help you understand why signals act so differently on a high speed digital system, identify the various problems that may occur in the design, and research solutions to minimize their impact and address their root causes. The authors offer a strong foundation that will help you get high speed digital system designs right the first time. Taking a systems design approach, High Speed Digital Design offers a progression from fundamental to advanced concepts, starting with transmission line theory, covering core concepts as well as recent developments. It then covers the challenges of signal and power integrity, offers guidelines for channel modeling, and optimizing link circuits. Tying together concepts presented throughout the book, the authors present Intel processors and chipsets as real-world design examples. Provides knowledge and guidance in the design of high speed digital circuits Explores the latest developments in system design Covers everything that encompasses a successful printed circuit board (PCB) product Offers insight from Intel insiders about real-world high speed digital design This book provides a thorough overview of cutting-edge

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research on electronics applications relevant to industry, the environment, and society at large. It covers a broad spectrum of application domains, from automotive to space and from health to security, while devoting special attention to the use of embedded devices and sensors for imaging, communication and control. The book is based on the 2019 ApplePies Conference, held in Pisa, Italy in September 2019, which brought together researchers and stakeholders to consider the most significant current trends in the field of applied electronics and to debate visions for the future. Areas addressed by the conference included information communication technology; biotechnology and biomedical imaging; space; secure, clean and efficient energy; the environment; and smart, green and integrated transport. As electronics technology continues to develop apace, constantly meeting previously unthinkable targets, further attention needs to be directed toward the electronics applications and the development of systems that facilitate human activities. This book, written by industrial and academic professionals, represents a valuable contribution in this endeavor.

A substantial update of his earlier book "Modern Electronic Test and Measuring Instruments" (IEE, 1996), the author provides a state-of-the art review of modern families of digital instruments. For each family he covers internal design, use and applications, highlighting their advantages and limitations from a practical application viewpoint. New enabling semiconductor technology including data converters, signal processors and modern sensors offers new capabilities to instrument designers and the book treats new digital instrument families such as DSOs, Arbitrary Function Generators, FFT analysers and many other common systems used by the test

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engineers, designers and research scientists.

Since publication of the 1st edition in 2002, there has been a deep evolution of the global communication network with the entry of submarine cables in the Terabit era. Thanks to optical technologies, the transmission on a single fiber can achieve 1 billion simultaneous phone calls across the ocean! Modern submarine optical cables are fueling the global internet backbone, surpassing by far all alternative techniques. This new edition of Undersea Fiber Communication Systems provides a detailed explanation of all technical aspects of undersea communications systems, with an emphasis on the most recent breakthroughs of optical submarine cable technologies. This fully updated new edition is the best resource for demystifying enabling optical technologies, equipment, operations, up to marine installations, and is an essential reference for those in contact with this field. Each chapter of the book is written by key experts of their domain. The book assembles in a complementary way the contributions of authors from key suppliers acting in the domain, such as Alcatel-Lucent, Ciena, NEC, TE-Subcom, Xtera, from consultant and operators such as Axiom, OSI, Orange, and from University and organization references such as TelecomParisTech, and Suboptic. This has ensured that the overall topics of submarine telecommunications is treated in a quite ecumenical, complete and un-biased approach. Features new content on: Ultra-long haul submarine transmission technologies for telecommunications Alternative submarine cable applications, such as scientific or oil and gas Addresses the development of high-speed

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networks for multiplying Internet and broadband services with: Coherent optical technology for 100Gbit/s channels or above Wet plant optical networking and configurability Provides a full overview of the evolution of the field conveys the strategic importance of large undersea projects with: Technical and organizational life cycle of a submarine network Upgrades of amplified submarine cables by coherent technology

PROVEN TECHNIQUES FOR GENERATING HIGH-FIDELITY MEASUREMENTS Power Integrity:

Measuring, Optimizing, and Troubleshooting Power Related Parameters in Electronics Systems provides field-tested techniques for producing high-fidelity measurements using the appropriate equipment. The book thoroughly discusses measurement guidelines, test instrument selection and use, connecting the equipment to the device being tested, and interpreting the acquired data. The latest electronics technologies and their impact on measurement are discussed. Detailed photographs, screenshots, schematics, and equations are included throughout this practical guide. Learn how to accurately measure: Impedance Stability Power supply rejection ratio (PSRR) Reverse transfer and crosstalk Step load response Ripple and noise Edges High-frequency impedance

This book describes the most frequently used high-speed serial buses in embedded systems, especially those used by FPGAs. These buses employ SerDes, JESD204, SRIO, PCIE, Aurora and SATA protocols for chip-to-chip and board-to-board communication, and CPCIE, VPX, FC and Infiniband protocols for inter-

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chassis communication. For each type, the book provides the bus history and version info, while also assessing its advantages and limitations. Furthermore, it offers a detailed guide to implementing these buses in FPGA design, from the physical layer and link synchronization to the frame format and application command. Given its scope, the book offers a valuable resource for researchers, R&D engineers and graduate students in computer science or electronics who wish to learn the protocol principles, structures and applications of high-speed serial buses.

Physics of New Materials After the discoveries and applications of superconductors, new ceramics, amorphous and nano-materials, shape memory and other intelligent materials, physics became more and more important, comparable with chemistry, in the research and development of advanced materials. In this book, several important fields of physics-oriented new-materials research and physical means of analyses are selected and their fundamental principles and methods are described in a simple and understandable way. It is suitable as a textbook for university materials science courses.

The essential preview guide to getting started with Raspberry Pi® computing and programming Originally conceived of as a fun, easy way for kids (and curious adults) to learn computer programming, the Raspberry Pi quickly evolved into a remarkably robust, credit-card-size computer that can be used for everything from playing HD videos and hacking around with hardware to learning to program! Co-authored by one of the creators of the

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Raspberry Pi, this special preview eBook fills you in on everything you need to know to get up and running on your Raspberry Pi in no time, including how to:

- Connect to a keyboard, mouse, monitor and other peripherals
- Install software
- Master basic Linux system administration
- Configure your Raspberry Pi
- Connect to wired or wireless networks
- Diagnose and troubleshoot common problems
- Use the GPIO port to flash an LED or read a button

Meet the Raspberry Pi provides a sneak peek preview of how to make the most out of the world's first truly compact computer.

For newcomers cast into the waters to sink or swim as well as seasoned professionals who want authoritative guidance desk-side, this hefty volume updates the previous (1999) edition. It contains the work of expert contributors who rallied to the job in response to a committee's call for help (the committee was assigned to the update by the Electron

The worldwide video game console market surpassed \$10 billion in 2003. Current sales of new consoles is consolidated around 3 major companies and their proprietary platforms: Nintendo, Sony and Microsoft. In addition, there is an enormous installed "retro gaming" base of Ataria and Sega console enthusiasts. This book, written by a team led by Joe Grand, author of "Hardware Hacking: Have Fun While Voiding Your Warranty", provides hard-core gamers with they keys to the kingdom: specific instructions on how to crack into their console and make it do things it was never designed to do. By definition, video console game players like to have fun. Most of them are addicted to the adrenaline

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rush associated with "winning", and even more so when the "winning" involves beating the system by discovering the multitude of "cheats" built into most video games. Now, they can have the ultimate adrenaline rush---actually messing around with the soul of the machine and configuring it to behave exactly as the command. This book builds on the motto of "Have Fun While Voiding Your Warranty" and will appeal to the community of hardware geeks who associate unscrewing the back of their video console with para-jumping into the perfect storm. Providing a reliable, field-tested guide to hacking all of the most popular video gaming consoles Written by some of the most knowledgeable and recognizable names in the hardware hacking community Game Console Hacking is the first book on the market to show game enthusiasts (self described hardware geeks) how to disassemble, reconfigure, customize and re-purpose their Atari, Sega, Nintendo, Playstation and Xbox systems

This book describes the fundamentals of THz communications, spanning the whole range of applications, propagation and channel models, RF transceiver technology, antennas, baseband techniques, and networking interfaces. The requested data rate in wireless communications will soon reach from 100 Gbit/s up to 1 Tbps necessitating systems with ultra-high bandwidths of several 10s of GHz which are available only above 200 GHz. In the last decade, research at these frequency bands has made significant progress, enabling mature experimental demonstrations of so-called THz communications, which are thus expected to

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play a vital role in future wireless networks. In addition to chapters by leading experts on the theory, modeling, and implementation of THz communication technology, the book also features the latest experimental results and addresses standardization and regulatory aspects. This book will be of interest to both academic researchers and engineers in the telecommunications industry.

Is your memory hierarchy stopping your microprocessor from performing at the high level it should be? *Memory Systems: Cache, DRAM, Disk* shows you how to resolve this problem. The book tells you everything you need to know about the logical design and operation, physical design and operation, performance characteristics and resulting design trade-offs, and the energy consumption of modern memory hierarchies. You learn how to tackle the challenging optimization problems that result from the side-effects that can appear at any point in the entire hierarchy. As a result you will be able to design and emulate the entire memory hierarchy. Understand all levels of the system hierarchy -Xcache, DRAM, and disk. Evaluate the system-level effects of all design choices. Model performance and energy consumption for each component in the memory hierarchy.

"Having been born a freeman, and for more than thirty years enjoyed the blessings of liberty in a free State—and having at the end of that time been kidnapped and sold into Slavery, where I remained, until happily rescued in the month of January, 1853, after a bondage of twelve years—it has been suggested that an account of my life and fortunes would not be uninteresting to the public."

-an excerpt

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"This book offers concepts of the teaching and learning of computer networking and hardware by offering fundamental theoretical concepts illustrated with the use of interactive practical exercises"--Provided by publisher.

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